

ACRONYMS AND ABBREVIATIONS

AAU	address arithmetic unit
AB	address bus
A/D	analog-to-digital conversion
ADPCM	adaptive differential pulse-code modulation
ASCII	American Standard code for information inter- change
ATG	automatic test generation
ATP	all tests passed
AU	arithmetic unit
AUC	arithmetic unit control register
BA	block averaging
BCD	binary coded decimal
BEF	band elimination filter
BP	bandpass
BPF	bandpass filter
CAD	computer-aided design
CAROT	centralized automatic reporting on trunks
CCITT	International Telegraph and Telephone Consulta- tive Committee (Comite Consultatif International Telegraphique et Telephonique)
CLKIN	clock input (also pin)
CLKOUT	clock output (also pin)
CPU	central processing unit
CST	control input (also pin)
CTR	clear to read (pin)
CTS	clear to send (pin)
CTU	carrier terminal units
CU	control unit
D/A	digital-to-analog conversion
DBS	multiplexed address and data bus (pins)
DFT	discrete Fourier transform
DIP	dual in-line package
DI	data input (pin)
DMA	direct memory access
DO	data output (pin)
DSP	digital signal processor
DSPMATE	hardware development system for DSP
dpsim	digital signal processor simulator
DTTU	digital treatment transmission unit
ESS	electronic switching system
<u>EXM</u>	external memory control (pin)
<u>EXE</u>	external memory control (pin)

FFT	fast Fourier transform
FIFO	first-in-first-out
FIR	finite impulse response
FSK	frequency shift keying
HGBEF	high-group band elimination filter
IBF	input buffer full (pin)
IBUF	input buffer register
ICK	input clock (pin)
IDFT	inverse discrete Fourier transform
IIR	infinite impulse response
I/O	input/output
IOAC	I/O, AAU, and control sections of DSP
IOC	register for DSP
IR	instruction register
IS	input stack
ISY	input synchronization (pin)
LAMP	logic analyzer for maintenance planning
LC	loop counter
LGBEF	low-group band elimination filter
LIU	line interface unit
LPC	linear predictive coding
LPF	low-pass filter
LSI	large-scale integration
LSL(LOCAL)	logic simulation language
LTX	computer-aided design system
MF	multifrequency
MOSFET	metal-oxide-semiconductor field effect transistor
MOS	metal-oxide semiconductor
MSI	medium-scale integration
NMOS	N-channel metal-oxide semiconductor
OBE	output buffer empty (pin)
OBUF	output buffer register
OCK	output clock (pin)
OS	output stack
OSY	output synchronization (pin)
PBX	private branch exchange
PC	program counter
PCM	pulse-code modulation
PR	program return
PROM	programmable read-only memory
QMFB	quadrature mirror filter bank
RAM	random access memory
RD	digital signal processor destination address register

RDA	digital signal processor auxiliary destination address register
REST	digital signal processor reset pin
ROM	read only memory
RX	digital signal processor X source address register
RY	digital signal processor Y source address register
RYA	digital signal processor auxiliary Y source address register
SBC	sub-band coding
SNR	signal-to-noise ratio
SOS	second-order section
SPICE	simulation program with integrated circuit emphasis
SSI	small-scale integration
STB	synchronization pulse (pin)
STR	digital signal processor status register
SYC	digital signal processor synchronization register
TTL	transistor-transistor logic
VCC	power supply (pin)
VF	voice frequency
VLSI	very large-scale integration
VSS	ground (pin)
XTAL	crystal connection (pin)

